



Applicant	William O'Leary	INFORMATION DISCLOSURE STATEMENT FORM PTO-1449
Serial No.	09/659,235	
Filing Date	September 11, 2000	
Group Art Unit	2152	
Examiner Name	Unknown	
Attorney Docket No.	100.136US01	
Title: PHASE COMPARATOR FOR A PHASE LOCKED LOOP		
Sheet 1 of 1		

U.S. Patent References				
Examiner Initials	Patent No.	Issue Date	Name	Filing Date
DmB ↓ DmB	5,239,561	08/24/93	Wong et al.	07/15/1991
	5,373,255	12/13/94	Bray et al.	07/28/1993
	5,376,847	12/27/94	Staszewski	12/30/1992
	5,726,607	03/10/98	Brede et al.	02/04/1994
	5,734,273	03/31/98	Mudd	03/15/1995
	5,790,614	08/04/98	Powell	07/02/1996
	5,990,673	11/23/99	Forsberg	06/30/1995
	6,064,273	05/16/00	Donohue	06/04/1998

Foreign Patent References					
Examiner Initials	Foreign Patent		Name	Publication Date	T?
	Country	No.			
	NONE				

Other References	
Examiner Initials	Author, Title, Date, Pages, etc.
DmB	Steven L. Maddy, "Phase-Locked Loop", <u>The Electrical Engineering Handbook [Computer File]</u> , Chapter 76, 1999.
DmB	Dan H. Wolaver, "Phase Locked Loop Basics", "Loop Filters", "Phase Detectors", <u>Phase-Locked Loop Circuit Design</u> , Chapter 2, 3 and 4, pgs. 9-79, Prentice Hall, 1991.

RECEIVED
MAY 1 - 2001
Technology Center 2100

Examiner Signature	<i>Dennis M. Butler</i>	Date Considered	9-29-03
*Examiner: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.			